Lucent Technologies
Bell Labs Innovations

MAP/5P and MAP/5PV3 Configuration

Differences Between the MAP/5P and the MAP/5PV3

The Differences Between the MAP/5P and the MAP/5PV3 table lists the differences between the MAP/5P and the MAP/5PV3 (MAP/5P Version 3):

NOTE:
Both versions support the same features and feature capacities.

Table: Differences Between the MAP/5P and the MAP/5PV3

Characteristic	MAP/5P	MAP/5PV3
COM2 location	Back, on the left side of the case above the parallel port	Back, under the arch
Sticker location	None	Back of the platform
Power outlets	Power inlet and outlet, back upper right corner	One power inlet, back upper right corner
Power cord	1 power cord that connects into the platform and 1 power cord that connects into the monitor and the platform	One power cord that connects into a Y-cord that connects to both the monitor and the platform
Front LED configuration	Middle LED is on only when the system is turned on.	Middle LED is a power available indicator that operates steadily when the power cord is connected
Memory	Four SIMMs	One DIMM

(1 of 2)

Table: Differences Between the MAP/5P and the MAP/5PV3

Characteristic	MAP/5P	MAP/5PV3
Motherboard and CPU	Socketed CPU	Slotted CPU

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End of section.

Viewing the CMOS Parameter Settings (MAP/5P and MAP/5PV3)

To view the CMOS Parameter Settings:

1. Start at the Lucent Intuity Main Menu and select

   ```
   Customer/Services Administration
      System Verification
        View CPU CMOS Settings
   ```

   The system displays the MAP/5P or MAP/5PV3 View CPU CMOS Settings Window.

2. Check the CMOS settings:

   - If the first entry on the screen identifies the system as a 133-MHz Classic or a 200-MHz MMX®, use the MAP/5P CMOS Parameter Settings table.
   - If the first entry on the screen identifies the system as a 450MHz Pentium® III, use the MAP/5PV3 CMOS Settings table.

3. Do one of the following:

   - To change any settings, continue with Changing the CMOS Settings (MAP/5P and MAP/5PV3).
   - To verify the settings, continue with Verifying the CMOS Parameter Settings (MAP/5P and MAP/5PV3).

- If you do not need to change or verify the settings, use the back button on the purple toolbar to return to your previous location.

Procedure completed.

Verifying the CMOS Parameter Settings (MAP/5P and MAP/5PV3)

To verify the CMOS Parameter Settings:

1. Start at the Lucent Intuity Main Menu and select

   ```
   Customer/Services Administration
      System Verification
        Verify CPU CMOS Settings
   ```

   The system displays the Verify CPU CMOS Settings Window (MAP/5P and MAP/5PV3).

2. Use the arrow keys to scroll to the bottom of the file and look for a message stating that all CMOS settings are valid.

3. Do one of the following:

   - If the system displays the message that the CMOS settings are valid, the settings are correct. Use the back button the back button on the purple toolbar to return to your previous location.
   - If the system does not display the message that the CMOS settings are valid and if it flagged any setting as not being a default setting, the settings are incorrect. Go to Changing the CMOS Settings (MAP/5P and MAP/5PV3).

Procedure completed.

Changing the CMOS Settings (MAP/5P and MAP/5PV3)

1. When prompted after the memory check, press CONTROL + ALT + ESCAPE

   The system displays the BIOS Configuration Screen (MAP/5P Only) or the Setup Utility Screen (MAP/5PV3 Only).

2. Do one of the following:

   - If the system is a MAP/5P, continue with Step 3.
   - If the system is a MAP/5PV3, press F8 at the setup main menu. This causes the system to display the advanced options under all of the menu selections

3. Make any necessary changes. Use the up ▲ and down ▼ arrows to move the cursor between fields. Use the left ◄ and right ► arrows to change the value of the field. Use the ESCAPE key to return to the previous menu.

   - If the system is a MAP/5P, use CMOS Parameter Settings (MAP/5P Only) to make any necessary changes.
   - If the system is a MAP/5PV3, use CMOS Setup Utility Values (MAP/5PV3 Only) to make any necessary changes.

Continue, press PAGE DOWN.

4. When you have completed changing the CMOS parameter settings, press ESCAPE.

   The system displays the following message:

   ```
   Do you want to save the CMOS settings?

   YES          NO
   ```

5. Place the cursor on `YES`.
6. Press ENTER.
7. Allow the system to boot.
8. Verify the CMOS settings.
9. Place a test call to the Lucent Intuity system to verify system operation.

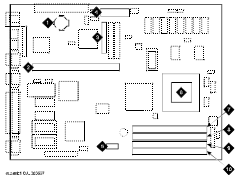
Procedure completed.

Memory Description (MAP/5P and MAP/5PV3)

Click on the graphic for a larger view.

The two versions support two different types of memory.

MAP/5P Motherboard

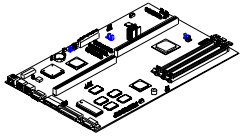


The MAP/5P supports 128 MB of memory packaged on four 32-MB single in-line memory modules (SIMMs). These modules are placed in sockets located in the bottom left corner of the CPU circuit card. The CPU circuit card must be equipped with SIMMs in matched pairs.

The MAP/5PV3 supports 128 MB of memory packaged on one dual in-line memory module (DIMM). This module is placed in one of three sockets located below the CPU. DIMMs do not need to be equipped in matched pairs.

End of section.

MAP/5PV3 Motherboard

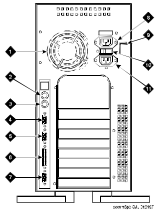


Platform Circuit Card Assignments (MAP/5P and MAP/5PV3)

Click on the graphic for a larger view.

Circuit cards are placed in the slots provided by the riser card in the MAP/5P and MAP/5PV3. Slots are numbered PCI 1 through PCI 3, and ISA 2 through ISA 6, from the top to the bottom of the circuit card cage. This section provides the fixed and variable assignments.

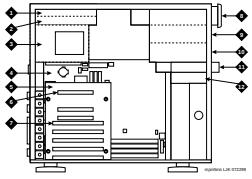
Back View



The following slot assignments are fixed. These slots can only be used for these cards:

- PCI Slot 1 — SCSI controller circuit card (Optional. This card is used only for certain upgrades or migrations.)
- PCI Slot 2 — PCI LAN circuit card (Optional)
- ISA Slot 2 — remote maintenance circuit card (Required)

Internal Layout



The other optional and required circuit cards have variable assignments, depending on what is installed. The following rules assume that PCI Slot 1, PCI Slot 2, and ISA Slot 2 are reserved.

- The system supports a maximum of three Tip/Ring circuit cards.
- The system supports all other circuit cards as one per system.
- Assign the tip/ring circuit cards sequentially, starting at the bottom with ISA Slot 6.
- Assign an ACCX circuit card, if provided, to the lowest numbered available slot after all tip/ring circuit cards are installed. For example, if ISA Slot 5 and ISA Slot 6 have tip/ring circuit cards installed, place the ACCX circuit card in ISA Slot 4.

Continue, press PAGE DOWN.

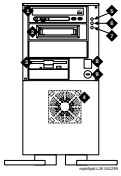
- Assign the switch interface circuit card, if provided, to the lowest-numbered available ISA slot.
- Assign the multi-port serial circuit card or super serial circuit card, if provided, to the lowest-numbered ISA slot after the switch interface circuit card, if any, is installed.
- If the system has unoccupied ISA slots, allow them to remain between the set of Tip/Ring and ACCX circuit cards and the set of switch interface and multi-port serial or super serial circuit cards.
- The system does not support both the DCIU circuit card and the digital station interface circuit card at the same time.
- The system does not support both the multi-port serial circuit card and the super serial circuit card at the same time.

End of section.

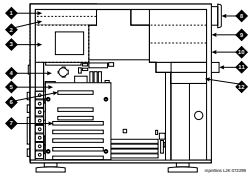
Platform Operating Hardware Assignments (MAP/5P and MAP/5PV3)

Click on the graphic for a larger view.

Front View



Internal Layout



Platform operating hardware is placed in bays numbered 1 through 7. Bays 1 through 5 are accessible from the front of the platform.

Bays 6 and 7 are accessible only after the dress cover has been removed.

The following bay assignments are fixed in the system:

- Bay 1 — CD-ROM drive
- Bay 2 — Tape drive
- Bay 3 — Empty
- Bay 4 — Diskette drive
- Bay 5 — Empty
- Bay 6 — Hard disk drive
- Bay 7 — Empty

End of section.

Resource Allocation (MAP/5P and MAP/5PV3)

The MAP/5P and MAP/5PV3 Resource Allocation table lists the resource assignments for all devices. It also includes the circuit cards.

Table: MAP/5P and MAP/5PV3 Resource Allocation

Device	IRQ	I/O Address	RAM Address	Notes
Video controller Included on motherboard	–	Plug & Play	A0000-BFFFF C0000-C7FFF	128 KB video RAM required 32 KB video BIOS required
System BIOS	–	-	E0000-FFFFF	Located on the motherboard, required
LPT1 port	7	378-37F	-	Located on the motherboard, required
COM1 port	4	3F8-3FF	-	Located on the motherboard, required
COM2 port	3	2F8-2FF	-	Located on the back of the MAP. Disable for remote maintenance circuit card
IDE primary channel	14	1F0-1F7	-	Located on the motherboard
IDE secondary channel				
PCI SCSI	11	Plug & Play	C8000-CFFFF	SCSI ID 7; optional. Not used for routine system operation.

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Table: MAP/5P and MAP/5PV3 Resource Allocation

Device	IRQ	I/O Address	RAM Address	Notes
IDE disk	–	–	–	One required: 8.4 for the MAP/5P 13.0 for the MAP/5PV3
Tape drive	–	–	–-	One required
Diskette drive	6	3F0-3F7	-	DMA 2, controller located on motherboard, required
CD-ROM drive	–	–	–	One required
PCI LAN 10 or 100MB circuit card	10	Plug & Play		One optional
Multi-port serial circuit card	–	–	D0000-D3FFF	One optional, not allowed with super serial circuit card
Super serial circuit card	–	260-27F	D0000-D3FFF	One optional, not allowed with multi-port serial circuit card
Tip/Ring circuit card	2	x00-x1F	–	x=1-3; One required, two optional
ACCX circuit card	5	140-14F	–	One optional
DCIU interface circuit card	12	240-24F	D4000-D7FFF	One optional; not allowed with VB-PC
Digital station interface circuit card	12	224-227	–	One optional, not allowed with DCIU interface circuit card

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Table: MAP/5P and MAP/5PV3 Resource Allocation

Device	IRQ	I/O Address	RAM Address	Notes
Remote maintenance circuit card	3	180-187	DC000-DCFFF	COM2 must be disabled to allow this circuit card to function

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End of section.

Platform Ferrites (MAP/5P and MAP/5PV3)

The MAP/5P and MAP/5PV3 Ferrite Installation table lists the ferrites to be installed on the systems.

NOTE:
The components listed below may not be present on your system.

Table: MAP/5P and MAP/5PV3 Ferrite Installation

Component	Location	Cabling
Tip/Ring circuit card (AYC29)	Place one ferrite on each modular cable.	Wrap the modular cable once around each ferrite.
Tip/Ring circuit card (AYC30)	Place two ferrites on each modular cable.	Wrap the modular cable once around each ferrite.
Remote maintenance circuit card (AYC55)	Place one ferrite on each shielded cable.	Snap the ferrite onto the cable. Do not wrap the cable.

End of section.

CMOS Parameter Settings (MAP/5P Only)

Table: MAP/5P CMOS Parameter Settings

Option	Setting
Basic System Configuration	
Motherboard type	ACER
CPU Type	133-MHz Classic or 200-MHz MMX
Controller Type:	IDE
BIOS Version:	ACR25E00-M0C-970116-r01-A5
Video Display Mode	VGA
Diskette Drive A	3.5 inch, 1.44 MB
Diskette Drive B	None
Primary IDE Controller	Enabled
IDE Drive 0 Setting	Auto
IDE Drive 1 Setting	Auto
IDE Drive 2 Setting	Auto
IDE Drive 3 Setting	Auto
IDE Hard Drive 0	Type 2
IDE Hard Drive 1	None

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Table: MAP/5P CMOS Parameter Settings

Option	Setting
IDE Hard Drive 2	None
IDE Hard Drive 3	None
IDE Block Mode	Enabled
IDE PIO Mode	Enabled
Hard Disk Size >504 MB	Enabled
Large Memory Support Mode	Normal
Num Lock After Boot	Enabled
Memory Test	Enabled
Advanced System Configuration	
Internal CPU Cache	Enabled
External Cache	Enabled
ECC/Parity Mode Selection	ECC Mode
Memory at 15 MB - 16 MB Reserved for	System
EDO Page Mode Read Timing	X-2-2-2
Page Mode Read Timing	X-3-3-3
EDO or Page Mode Write Timing	X-3-3-3

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Table: MAP/5P CMOS Parameter Settings

Option	Setting
Pipelined Function	Enabled
CPU to PCI Write Buffer	Enabled
Linear-Merge	Enabled
Word-Merge	Enabled
Write-Burst	Enabled
Programmable Frame Buffer	Enabled
ISA Clock Select	Auto
DMA Line Buffer	Enabled
ISA Line Buffer	Enabled
ISA I/O Recovery	Enabled
ISA I/O Recovery Period	1.75 us
ISA Refresh Cycle	15 us
6x86 Burst Mode	1+4
Power Saving Configuration	
Power Management Mode	Disabled
System Security	
Diskette Drive Control	Normal

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Table: MAP/5P CMOS Parameter Settings

Option	Setting
Hard Disk Drive Control	Normal
System Boot Drive Sequence	Drive A then Drive C
Boot from CD-ROM	Disabled
Serial Port 1 Address	03F8
Serial Port 2 Address	Disabled
Parallel Port Address	0378 (IRQ 7)
Parallel Port Operation Mode	Standard
Onboard PS/2 Mouse (IRQ 12)	Disabled
PnP/PCI System Configuration	
PCI IRQ Setting	Manual
PCI Interrupt Slot 1 (INTA)	IRQ 11
PCI Interrupt Slot 2 (INTA)	IRQ 10
PCI Interrupt Slot 3 (INTA)	Disabled
On-Board VGA Interrupt	Disabled
PCI IRQ Sharing	No
VGA Palette Snoop	Disabled
Plug and Play OS	No

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CMOS Parameter Settings (MAP/5PV3 Only)

Table: MAP/5PV3 CMOS Settings

Option	Setting
CPU INFORMATION	
Mother Board Type	ACER
CPU Type	450Mhz-Pentium-III
Controller Type	IDE
Bios Version	ACR86000-I0C-990712-R03-B4 or similar ACR part number
Disk Drives	
Floppy Drive Present	Floppy Drive Installed
Number of Floppy Drives Installed	One Drive Installed
Floppy Drive A	3.5 inch, 1.44MB
Floppy Drive B	None
LS-120 Setting	LS-120 As Normal (None)
HDD 0 Auto-type Selected	Auto
IDE 0 Hard Disk Size > 504MB	Auto

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Table: MAP/5PV3 CMOS Settings

Option	Setting
IDE 0 Block Mode	Auto
IDE 0 Advanced PIO Mode	Auto
IDE 0 32-bit Access	Disabled
IDE 0 DMA Transfer Mode	Auto
HDD 1 Auto-type Selected	Auto
IDE 1 Hard Disk Size > 504MB	Auto
IDE 1 Block Mode	Auto
IDE 1 Advanced PIO Mode	Auto
IDE 1 32-bit Access	Disabled
IDE 1 DMA Transfer Mode	Auto
HDD 2 Auto-type Selected	Auto
IDE 2 Hard Disk Size > 504MB	Auto
IDE 2 Block Mode	Auto
IDE 2 Advanced PIO Mode	Auto
IDE 2 32-bit Access	Disabled
IDE 2 DMA Transfer Mode	Auto
HDD 3 Auto-type Selected	Auto

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Table: MAP/5PV3 CMOS Settings

Option	Setting
IDE 3 Hard Disk Size > 504MB	Auto
IDE 3 Block Mode	Auto
IDE 3 Advanced PIO Mode	Auto
IDE 3 32-bit Access	Disabled
IDE 3 DMA Transfer Mode	Auto
Onboard Peripherals	
Serial Port 1 Enable/Disable	Enabled
Serial Port 1 Base Address	03F8h
Serial Port 1 IRQ	IRQ4
Serial Port 2 Enable/Disable	Disabled
Serial Port 2 Base Address	02F8h
Serial Port 2 IRQ	IRQ3
Parallel Port Enable/Disable	Enabled
Parallel Port Base Address	0378h
Parallel Port IRQ	IRQ 7
Parallel Port Operation Mode	Standard
Onboard Floppy Disk Controller	Enabled

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Table: MAP/5PV3 CMOS Settings

Option	Setting
Primary IDE Controller	Enabled
Secondary IDE Controller	Enabled
Onboard PS/2 Mouse(IRQ12)	Disabled
Onboard USB Controller	Disabled
Onboard Audio Existence	Doesn't Exist
Onboard Audio Enable	Disabled
Math Coprocessor	Present
Power Management	
Power Management Control	Disabled
Boot Options	
Boot Sequence 1st	Floppy Disk
Boot Sequence 2nd	Hard Disk
First Hard Disk Drive	IDE
Primary Display Adapter	Onboard
Fast Boot	Disabled
Silent Boot	Disabled
Num Lock After Boot	Enabled

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Table: MAP/5PV3 CMOS Settings

Option	Setting
Memory Test	Enabled
System Configuration Table	Enabled
System Security	
Floppy Drive Control	Normal
Hard Disk Drive Control	Normal
Advanced Options: Memory/Cache Options	
Internal CPU Cache	Enabled
Internal CPU Cache Scheme	Write Back
External Cache	Enabled
External CPU Cache Scheme	Write Back
Memory at 15MB-16MB Reserved	For System Use
System Parity Checking	Enabled
Memory Parity Mode	ECC
C8000h - DFFFFh Shadow	Disabled
Advanced Options: PnP/PCI Options	
PCI IRQ Setting	Manual

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Table: MAP/5PV3 CMOS Settings

Option	Setting
PCI Interrupt Slot 1 (INTA)	Disabled, or if a SCSI card was installed during an upgrade, the system may report IRQ11
PCI Interrupt Slot 2 (INTA)	Disabled, or if a LAN circuit card is installed, IRQ10
PCI Interrupt Slot 3 (INTA)	Disabled
PCI IRQ Sharing	No
VGA Palette Snoop	Disabled
Graphics Aperture Size	64MB
Plug and Play OS	No
Advanced Configuration Information	
CD-ROM Bootable	Enabled
Primary Display Video Mode	Reserved
System Region Cacheable	Enabled
Video Region Cacheable	Enabled
Large Memory Support Mode	Normal
CPU to PCI Delayed Transaction	Enabled
CPU-to-PCI IDE Write Posting	Enabled
WC Write Posting	Disabled

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Table: MAP/5PV3 CMOS Settings

Option	Setting
PCI 32 Clock Target Timer	Enabled
PCI-to-DRAM Pipeline	Enabled
Burst Write Combining	Enabled
Read Around Write	Enabled
SDRAM RAS to CAS Delay	3
SDRAM CAS Latency	3
SDRAM RAS Pre-charge Time	3
MA Wait State	Slow
Spread Spectrum	Normal
Cacheable Region F	Disabled
Cacheable Region C	Disabled
16-bit I/O Recovery Time	1
Hardware Monitor	Enabled
Power Button	Power Off

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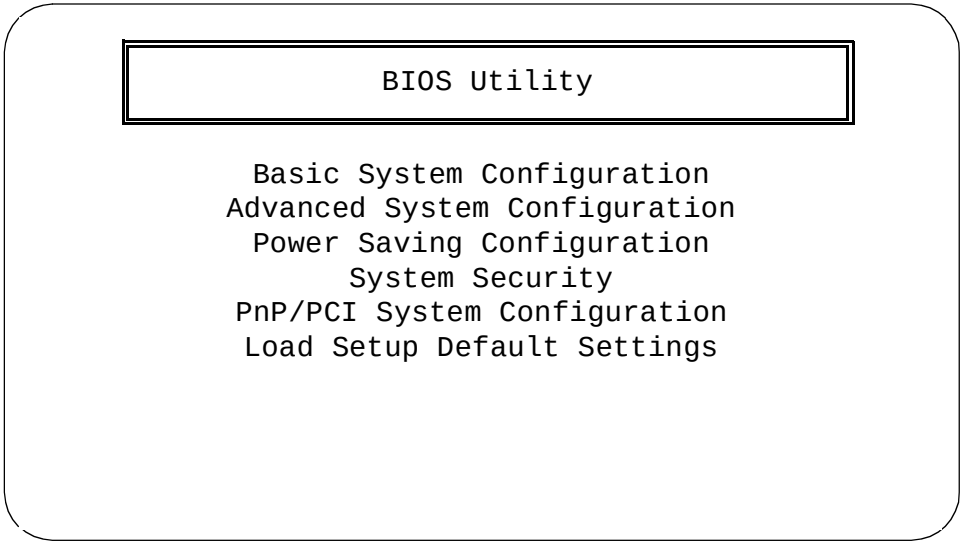
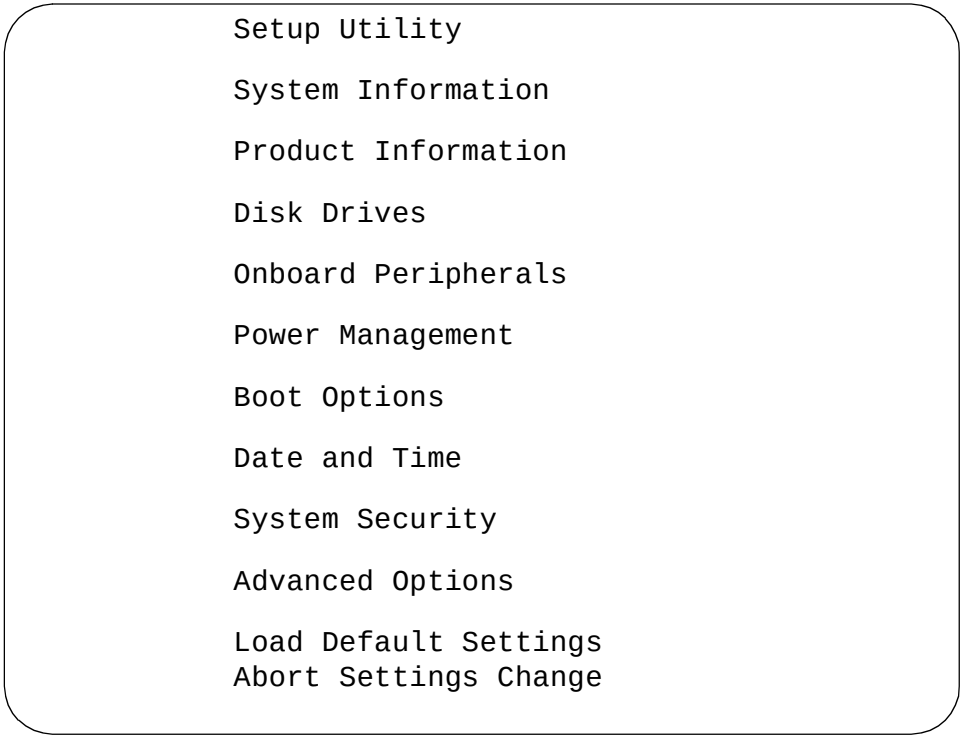
BIOS Configuration Screen (MAP/5P Only)



Setup Utility Screen (MAP/5PV3 Only)



CMOS Setup Utility Values (MAP/5P Only)

Table: MAP/5P CMOS Setup Utility Values

Option	Setting
Basic System Configuration	
Date	month/day/year (as required)
Time	hour/minutes/seconds (as required)
Diskette Drive A	1.44 MB 3.5-inch
Diskette Drive B	None
IDE Drive 0 (8063 MB)	Auto, Cylinder: 16383, Head: 16, Sector: 63
IDE Drive 1 (0 MB)	Auto, Cylinder: 0, Head: 0, Sector: 0
IDE Drive 2 (0 MB)	Auto, Cylinder: 0, Head: 0, Sector: 0
IDE Drive 3 (0 MB)	Auto, Cylinder: 0, Head: 0, Sector: 0
Onboard IDE Controller	Enabled
Total Memory	128 MB

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Table: MAP/5P CMOS Setup Utility Values

Option	Setting
Basic System Configuration, Enhanced IDE Features (Page Down)	
Hard Disk Block Mode	Enabled
Advanced PIO Mode	Enabled
Hard Disk Size > 504MB	Enabled
Hard Disk 32 Bit Access	Disabled
Large Memory Support Mode	Normal
Num Lock After Boot	Enabled
Memory Test	Enabled
Quiet Boot	Enabled
Configuration Table	Disabled
Advanced System Configuration	
Internal Cache (CPU Cache)	Enabled
External Cache	Enabled
Cache Scheme	Write Back
ECC/Parity Mode Selection	ECC

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Table: MAP/5P CMOS Setup Utility Values

Option	Setting
Memory at 15MB-16MB Reserved for	System Use
Power Saving Configuration	
Power Management Mode	Disabled
Power Saving Operation Mode	Traditional
IDE Hard Disk Standby Timer	Off
Monitor Power Saving Timer	Off
System Standby Timer	Off
System Suspend Timer	Off
Power Saving Configuration, Monitored Activities	
IRQ2/IRQ 9	Disabled
IRQ 3	Disabled
IRQ 4	Disabled
IRQ 5	Disabled
IRQ 7	Disabled
IRQ 10	Disabled

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Table: MAP/5P CMOS Setup Utility Values

Option	Setting
IRQ 11	Disabled
IRQ 12	Disabled
IRQ 15	Disabled
QuickStart State Timer	Off
Point Device Location	None
System Security Selection, Disk Drive Control	
Diskette Drive	Normal
Hard Disk Drive	Normal
System Boot Drive	Drive A Then C
Boot from CD-ROM	Disabled
System Security Selection, Onboard Communication Ports	
Serial Port 1 Base Address	3F8h
Serial Port 2 Base Address	Disabled
Parallel Port Base Address	378h(IRQ 7)
Operation Mode	Standard Parallel Port(SSP) Mode

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Table: MAP/5P CMOS Setup Utility Values

Option	Setting
ECP DMA Channel	-
Onboard PS/2 Mouse (IRQ 12)	Disabled
Setup Password	None
Power On Password	None
PnP/PCI System Configuration	
PCI IRQ Setting	Manual
PCI Slot 1	INTA 11, INTB 10, INTC --, INTD --
PCI Slot 2	INTA 10, INTB --, INTC --, INTD 11
PCI Slot 3	INTA --, INTB --, INTC 11, INTD 10
Onboard-Board VGA	--
PCI IRQ Sharing	No
VGA Palette Snoop	Disabled
Plug & Play OS	No
Rest Resources Assignment	No

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CMOS Setup Utility Values (MAP/5PV3 Only)

Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
System Information	
Processor	Pentium III
Processor Speed	450 MHz
Internal Cache	32 KB, Enabled
External Cache	512 KB, Enabled
Floppy Drive A	1.44MB 3.5-inch
Floppy Drive B	None
IDE Primary Channel Master	Hard Disk, 13030 M.B.
IDE Primary Channel Slave	None
IDE Secondary Channel Master	CD-RW
IDE Secondary Channel Slave	IDE CD-ROM
Total Memory	128 MB
1st Bank	SDRAM, 128 MB
2nd Bank	None

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
3rd Bank	None
(System Information—Page Down)	
Serial Port 1	3F8h, IRQ 4
Serial Port 2	Disabled
Parallel Port	378h, IRQ 7
PS/2 Mouse	None
Product Information	
Product Name	V66LA
System S/N	00000000000000 or the system's serial number
Main Board ID	V66LA
Main Board S/N	00000000000000 or the systems motherboard serial number
System BIOS Version	V3.2
SMBIOS Version	2.1
Disk Drives	
Floppy Drive A	1.44MB 3.5-inch

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
Floppy Drive B	None
LS-120 drive as	Normal
Disk Drives: IDE Primary Channel Master	
Type	Auto
Cylinder	------
Head	------
Sector	------
Size	13030 MB
Hard Disk Size > 504MB	Auto
Hard Disk Block Mode	Auto
Advanced PIO Mode	Auto
Hard Disk 32 Bit Access	Disabled
DMA Transfer Mode	Auto
Disk Drives: IDE Primary Channel Slave	
Type	Auto
Cylinder	0
Head	0

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
Sector	0
Size	0 MB
Hard Disk Size > 504MB	Auto
Hard Disk Block Mode	Auto
Advanced PIO Mode	Auto
Hard Disk 32 Bit Access	Disabled
DMA Transfer Mode	Auto
Disk Drives: IDE Secondary Channel Master	
Type	Auto
Cylinder	0
Head	0
Sector	0
Size	0 MB
Hard Disk Size > 504MB	Auto
Hard Disk Block Mode	Auto
Advanced PIO Mode	Auto
Hard Disk 32 Bit Access	Disabled

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
DMA Transfer Mode	Auto
Disk Drives: IDE Secondary Channel Slave	
Type	Auto
Cylinder	0
Head	0
Sector	0
Size	0 MB
Hard Disk Size > 504MB	Auto
Hard Disk Block Mode	Auto
Advanced PIO Mode	Auto
Hard Disk 32 Bit Access	Disabled
DMA Transfer Mode	Auto
Onboard Peripherals	
Serial Port 1	Enabled
Base Address	3F8h
IRQ	4
Serial port 2	Disabled

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
Base Address	----
IRQ	--
Parallel Port	Enabled
Base Address	378h
IRQ	7
Operation Mode	Standard
ECP DMA Channel	-
Onboard Peripherals: Onboard Device Settings	
Floppy Disk Controller	Enabled
IDE Controller	Both
PS/2 Mouse Controller	Disabled
USB Host Controller	Disabled
USB Legacy Mode	-----
Power Management	
Power Management Mode	Disabled
IDE Hard Disk Standby Timer	---
System Sleep Timer	-----

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
Sleep Mode	-------
Power Switch < 4 sec.	Power Off
System wake-up event Modem Ring Indicator	Disabled
Boot Options	
Boot Sequence: 1st.	Floppy Disk
2nd.	Hard Disk
3rd.	IDE CD-ROM
First Hard Disk Drive	IDE
Primary Display Adapter	Onboard
Fast Boot	Disabled
Silent Boot	Disabled
Num Lock After Boot	Enabled
Memory Test	Enabled
Configuration Table	Enabled
Date and Time	
Date	month day, year as required)
Time	hour:minutes:seconds (as required)

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Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
System Security	
Setup Password	None
Power-on Password	None
Operation Mode	Normal
Disk Drive Control: Floppy Drive	Normal
Hard Disk Drive	Normal
Processor Serial Number	Enabled
Advanced Options: Memory/Cache Options	
Internal Cache (CPU Cache)	Enabled
External Cache	Enabled
Cache Scheme	Write Through (Fixed field)
Memory at 15MB-16MB Reserved for	System
Memory Parity Mode	ECC
C8000h - DFFFFh Shadow	Disabled
Advanced Options: PnP/PCI Options	
PCI IRQ Setting	Manual
PCI Slot 1	INTA 11, INTB 10, INTC --, INTD --

(8 of 9)

Table: MAP/5PV3 CMOS Setup Utility Values

Option	Setting
PCI Slot 2	INTA 10, INTB --, INTC --, INTD 11
PCI Slot 3	INTA --, INTB --, INTC 11, INTD 10
PCI IRQ Sharing	No
VGA Palette Snoop	Disabled
Graphics Aperture Size	64 MB
Plug and Play OS	No
Reset Resource Assignments	No

(9 of 9)

Figure: Motherboard (MAP/5P Only)

1 CMOS battery
2 Riser card connector
3 Diskette cable connector
4 Power supply connectors
5 Switches
6 CPU
7 SIMM4 socket (empty)
8 SIMM3 socket (empty)
9 SIMM2 socket
10 SIMM1 socket

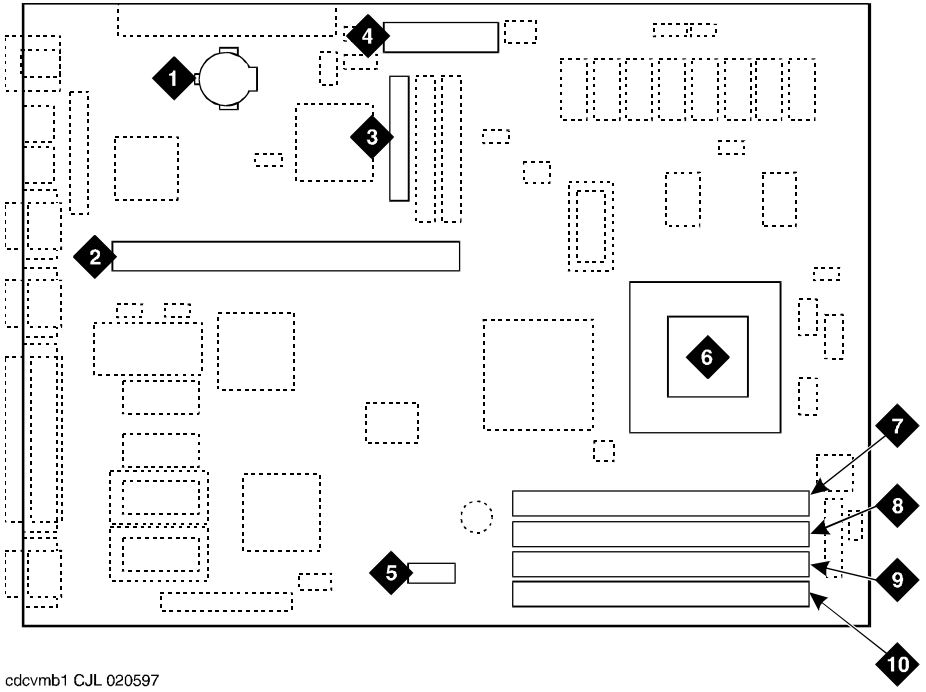

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Figure: Motherboard (MAP/5PV3 Only)

1 Riser card connector
2 CPU slot
3 DIMM sockets

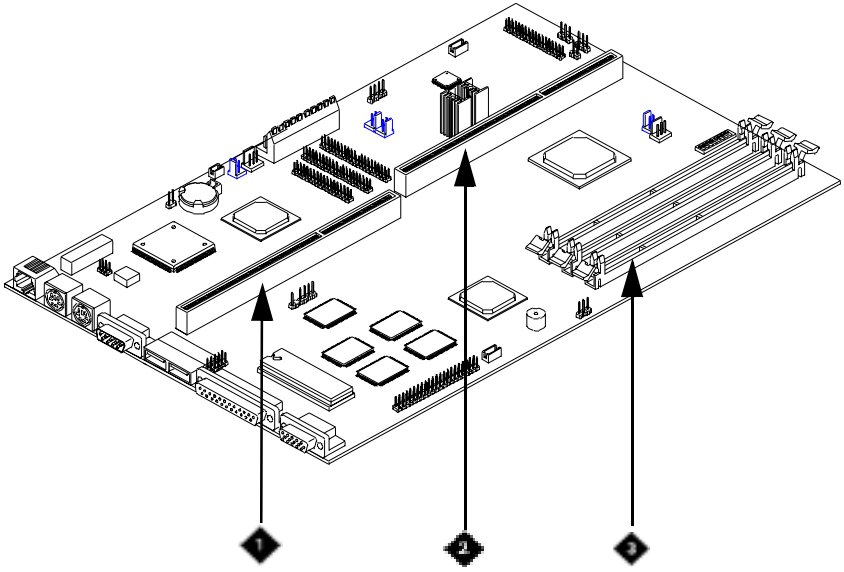

Figure: Back View (MAP/5P and MAP/5PV3)

1 Power supply fan exhaust
2 Keyboard connector
3 Mouse connector (not used)
4 COM1
5 COM2 (MAP/5P only) or USB ports (MAP/5PV3 only, not used)
6 Parallel port
7 Monitor connector
8 AC power supply outlet for the monitor (MAP/5P only)
9 Dress cover lock
10 AC voltage selector switch
11 AC power inlet receptacle
12 COM2 (MAP/5PV3 only)

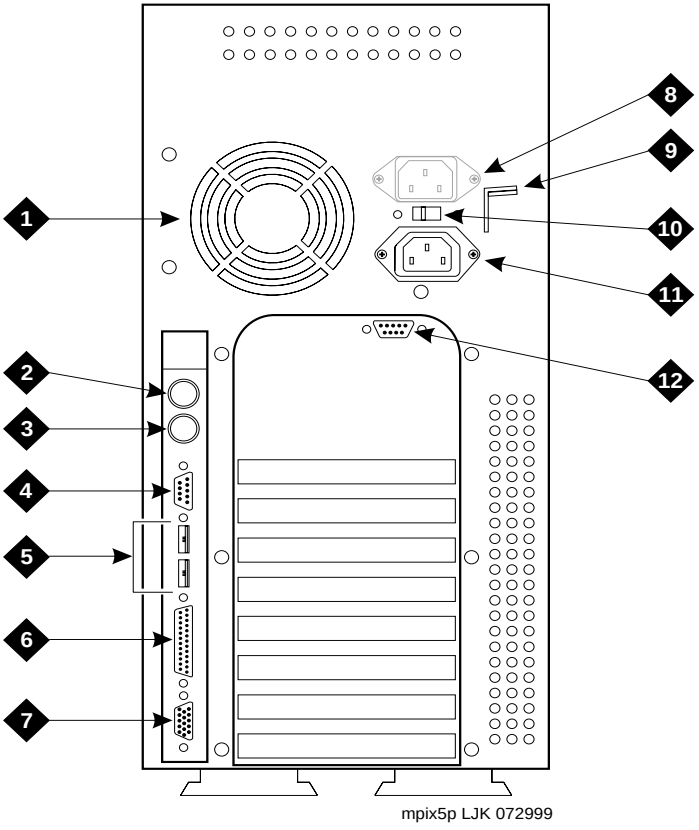

Figure: Front View (MAP/5P and MAP/5PV3)

1 CD-ROM drive
2 Tape drive
3 Diskette drive
4 Circuit card cage fan
5 Hard disk drive activity LED
6 Speed LED (MAP/5P only, not used) or Power Available LED (MAP/5PV3 only)
7 Power on LED
8 Power switch
9 Reset switch

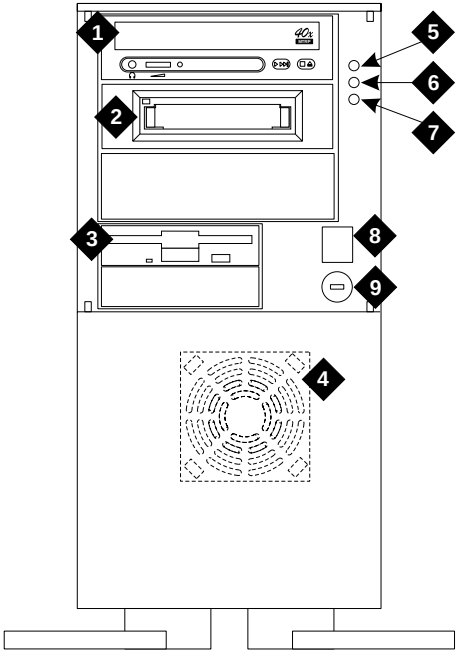

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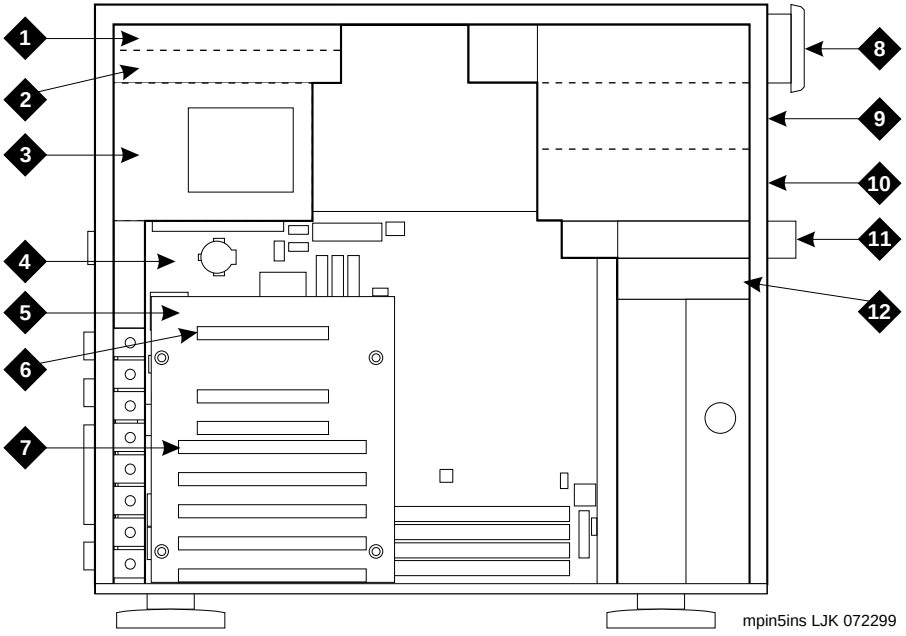
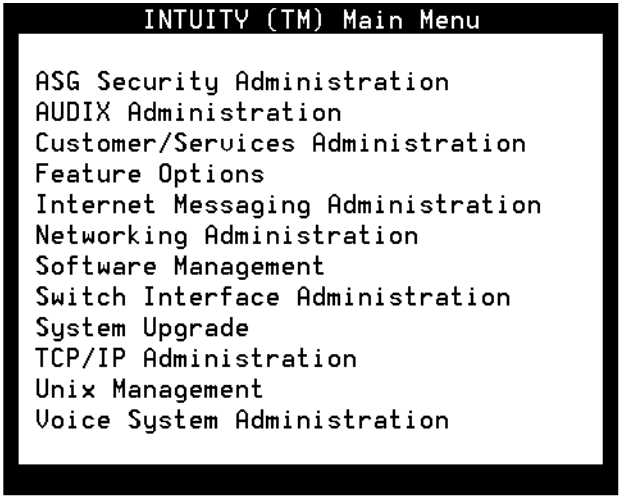
Figure: Internal Layout (MAP/5P and MAP/5PV3)

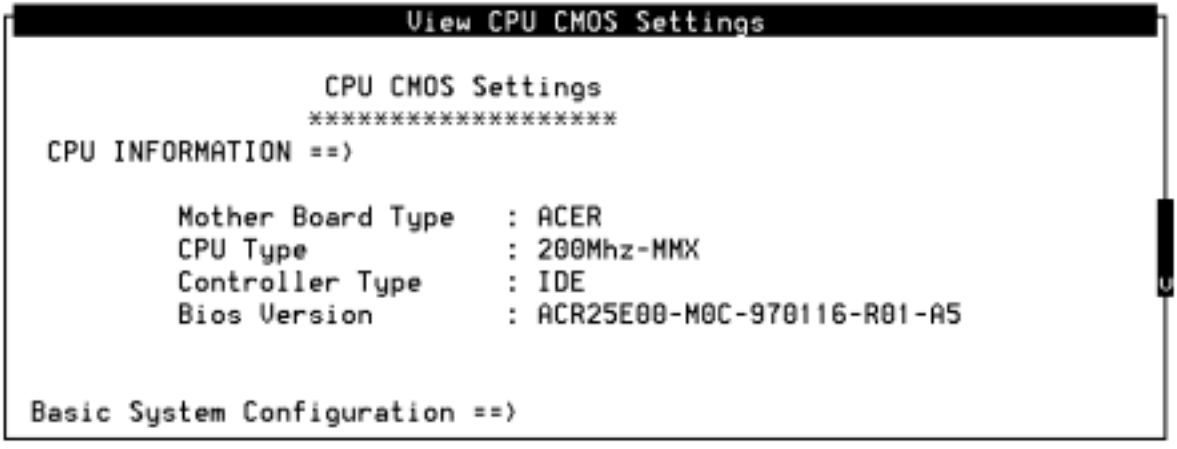
1 Bay 6 - hard disk drive
2 Bay 7 -empty
3 Power supply
4 Motherboard
5 Riser card
6 PCI Slot 1
7 ISA Slot 2
8 Bay 1 - CD-ROM drive
9 Bay 2 - tape drive
10 Bay 3 - empty
11 Bay 4 - diskette drive
12 Bay 5 - empty



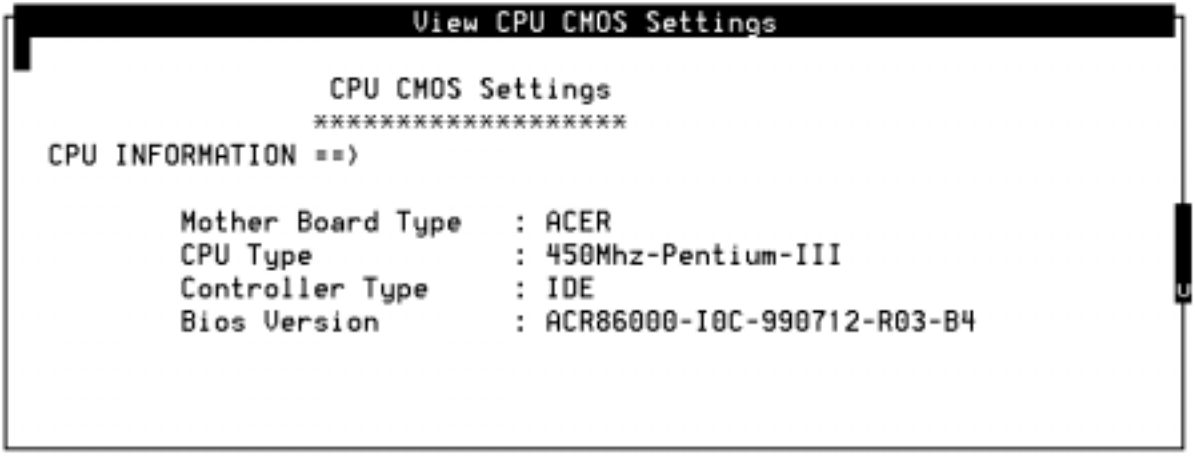
Lucent Intuity Main Menu



View CPU CMOS Settings Window (MAP/5P Only)



View CPU CMOS Settings Window (MAP/5PV3 Only)



Verify CPU CMOS Settings Window (MAP/5P and MAP/5PV3)